

<b>Notice of References Cited</b>	Application/Control No. 10/735,342		Applicant(s)/Patent Under Reexamination SHEI ET AL.	
	Examiner Shambhavi Patel		Art Unit 2128	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-2003/0074178	04-2003	Sample et al.	703/25
*	B	US-6,034,538	03-2000	Abramovici, Miron	326/38
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Morris, J.N. 'AnyBoard: an FPGA-Based, Reconfigurable System'. Design & Test of Computers, IEEE. September 1992, Volume 9. pages 21-30.
	V	Mohammed Khalid. 'Routing Architecture and Layout Synthese for Multi-FPGA Systems'. 1999.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.